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**EXAM ID NUMBER: \_\_\_\_\_**

**COURSE NUMBER: EE 203**

**PROBLEM: 1**

**Problem 1 (20 points):** You have at your disposition a specific set of tools and chemicals with the purpose of reproducing the cross-sectional profiles shown below. You are only allowed to use **one** photolithography step that can reach a critical dimension of  $1\ \mu\text{m}$ . For each of the profiles draw and briefly describe each of the steps that you would use to end up with the same profile. (The profile can consist of different materials unless otherwise stated).

**Tools available:**

- Photolithography set (Spin coating, mask aligner [min. feature is  $1\ \mu\text{m}$ ], developing)
- Thermal oxidation
- PECVD tool for  $\text{SiO}_2$ ,  $\text{Si}_3\text{N}_4$ , poly-silicon and amorphous silicon deposition.
- ICP-RIE tool for  $\text{SiO}_2$ ,  $\text{Si}_3\text{N}_4$ , silicon, poly-silicon and amorphous silicon etching.
- DRIE tool for silicon.

**Chemicals:**

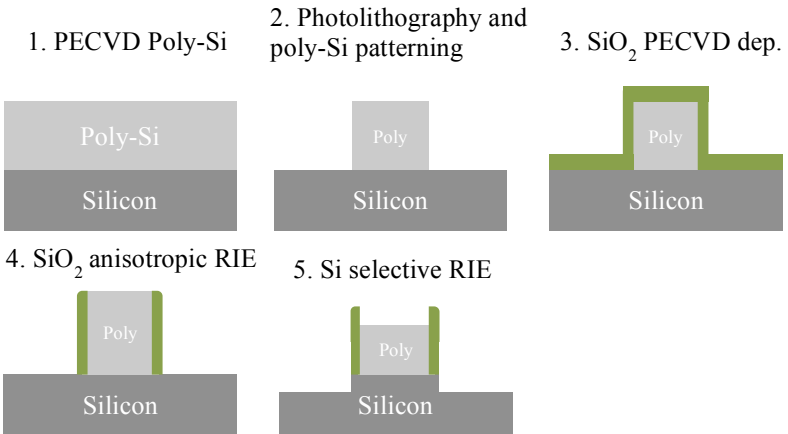
- $\text{NH}_4\text{F}$ ,  $\text{HF}$ ,  $\text{H}_3\text{PO}_4$ ,  $\text{HCl}$ ,  $\text{KOH}$  (hot plates available)
- $\text{XeF}_2$  (Chamber available for vapor-based etching)

**Example:**

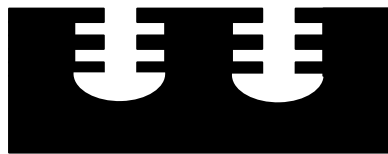
**Profile:**



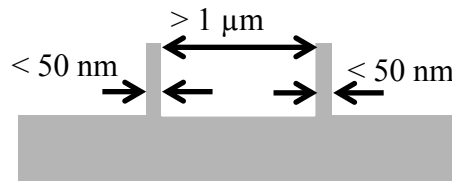
**Solution:**



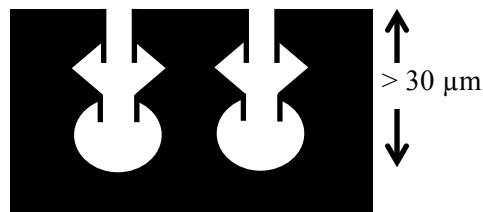
a. (3 points)



c. This profile is **only** Silicon (10 points)



b. (7 points)



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**PROBLEM: 2**

**Problem 2 (20 points):** You have joined a research group and have been assigned the task of fabricating high-k/metal gate MOSFET devices. The good news is that there is an old fabrication baseline record available; the bad news is, the different steps have been scrambled and some details have been lost. Below you will find different steps in no particular order, including recipes and tools. Use this information to organize and complete the fabrication baseline for pMOSFET. (*Accurate step order – 12 points (0.5 points for each step). Accurate recipe details – 8 points distributed as below*)

**Fabrication baseline for pMOSFET:**

1. Starting substrate: \_\_-type, \_\_\_\_\_ doped, 4" (\_\_\_\_) silicon wafer (0.5 points)
2. Ion implantation (I/I): Dose=  $5 \times 10^{13}$  cm<sup>2</sup> with \_\_\_\_ at 10 KeV (0.5 points)
3. Photolithography: ECI 3027: 4 μm, bright field mask, hard contact
4. Photolithography: ECI 3027: 4 μm, \_\_\_\_\_ field mask, soft contact. (0.5 points)
5. Photolithography: \_\_\_\_\_, AZ 1512: 1.4 μm, \_\_\_\_\_ field mask, V+H contact (0.5 points)
6. PR Removal: Acetone for 5 min.
7. PR Removal: Acetone for 5 min.
8. PR Removal: Acetone for 5 min.
9. RCA cleaning: (3 group points)
  - SPM/Piranha at \_\_\_\_ °C for 10 minutes (SPM/Piranha: H<sub>2</sub>SO<sub>4</sub>/\_\_\_\_ – 4:1) (1 points)
  - SC1 at 75°C for 10 minutes (SC1= \_\_\_\_\_/H<sub>2</sub>O<sub>2</sub>/H<sub>2</sub>O – 1:1:20) (0.5 points)
  - Diluted-HF at \_\_\_\_ for 30 seconds (0.5 points)
  - SC2 at 75°C for 10 minutes (SC2= \_\_\_\_\_/H<sub>2</sub>O<sub>2</sub>/H<sub>2</sub>O – 1:1:20) (0.5 points)
  - \_\_\_\_\_ at 40°C for 30 seconds. (0.5 points)
10. SPM/Piranha at \_\_\_\_ °C for 5 min. (+0.5 extra)
11. Vapor HF at \_\_\_\_ °C for 15 seconds. (+0.5 extra)
12. RTP: Annealing at \_\_\_\_ °C for 30s in 200 sccm Ar (0.5 points)
13. RTP: Annealing at 950°C for 30s in 200 sccm Ar
14. High Temperature Furnace: (1 group point)
  - \_\_\_\_ oxidation growth at 1100°C for 15 min.
  - \_\_\_\_ oxidation growth at 1100°C for 26 min.
  - \_\_\_\_ oxidation growth at 1100°C for 15 min.
15. ALD: High k/ metal gate stack deposition: (1 group point)
  - 10 nm \_\_\_\_ : 100 cycles, 300°C, TMA dose: 15ms, 80 mTorr, O<sub>2</sub> plasma.
  - 20 nm \_\_\_\_ : 200 cycles, 250°C, TiCl<sub>4</sub> dose: 0.2s, 230mTorr, Ar plasma.
16. PECVD: 100nm poly-silicon, \_\_\_\_ °C, 10W<sub>RF</sub>, 1000mTorr, 50 sccm \_\_\_\_, 450 sccm Ar (0.5)
17. PECVD: 50nm \_\_\_\_, 300°C, 20W<sub>RF</sub>, 850mTorr, 23sccm SiH<sub>4</sub>, 20sccm NH<sub>3</sub>, 980sccm N<sub>2</sub> (0.5)
18. Sputtering: Ni nm Ni, 400 W, 5 mTorr, 25 sccm Ar
19. Sputtering: 200 nm Ar, 500 W, 5 mTorr, 25 sccm Ar
20. RIE:
  - 80°C, 1500 W<sub>ICP</sub>, 50 W<sub>RF</sub>, 40 mTorr, 10 sccm Cl<sub>2</sub>, 40 sccm BCl<sub>3</sub>, 10 sccm Ar
  - 80°C, 1500 W<sub>ICP</sub>, 150 W<sub>RF</sub>, 20 mTorr, 40 sccm Cl<sub>2</sub>, 10 sccm BCl<sub>3</sub>;
  - 80°C, 1500 W<sub>ICP</sub>, 50 W<sub>RF</sub>, 40 mTorr, 10 sccm Cl<sub>2</sub>, 30 sccm BCl<sub>3</sub>, 20 sccm Ar;
  - 80°C, 150 W<sub>RF</sub>, 900 mTorr, 100 sccm O<sub>2</sub>
21. RIE: 800 W<sub>ICP</sub>, 70 W<sub>RF</sub>, 10 mTorr, 15 sccm SF<sub>6</sub>, 4 sccm O<sub>2</sub>
22. RIE: 1500 W<sub>ICP</sub>, 100 W<sub>RF</sub>, 10 mTorr, 5 sccm O<sub>2</sub>, 40 sccm CHF<sub>3</sub>
23. RIE: 2000 W<sub>ICP</sub>, 50 W<sub>RF</sub>, 10 mTorr, 10 sccm SF<sub>6</sub>, 90 sccm CHF<sub>3</sub>
24. RIE: 1500 W<sub>ICP</sub>, 100 W<sub>RF</sub>, 10 mTorr, 40 sccm C<sub>4</sub>F<sub>8</sub> + 5 sccm O<sub>2</sub>